

Location: Austin, TX
Interested in: Permanent-Hire Positions,
World-Wide Locations

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Michael C. Hollinger

- OBJECTIVE** Full-time software architecture and development. Interested in enterprise systems development or digital convergence applications as well as leadership opportunities.
- EDUCATION** University of Oklahoma, Norman OK – Accelerated M.S. E.E. / Computer Engr.
08 / 04 – 05 / 05
- 3.5 / 4.0 GPA with Thesis Option
 - Thesis Defended: *Server-Side Software For Multimodal User Interactions*
 - May, 2005 Graduation (M.S. Electrical and Computer Engineering)
- 08 / 00 – 05 / 04 University of Oklahoma, Norman, OK – B.S. Computer Engineering
- 3.8 / 4.0 GPA, Summa Cum Laude, including 16 hours honors credit
 - '03-'05 President of Eta Kappa Nu Electrical & Computer Engineering Honor Society, '04 Outstanding Senior in Computer Engr., 2004-05 Co-Chair IEEE OU Tech. Society
 - **Applicable Courses:** Engineering Leadership / Org. Management, SW Data Structures, Computer Architecture, Microprocessor Design, Computer Hardware Design, E.E. Labs & Design Lab, Signals and Systems, Robotics, Reconfigurable Computing, A.I., VHDL
- HONORS / ACTIVITIES / AWARDS**
- 2011 IBM Outstanding Technical Achievement, Delivery of POWER7 / Apollo
 - 2010 IBM "Bravo" Award, Delivery of Power 750
 - 2008 IBM Outstanding Innovation Award, Delivery of EnergyScale
 - 28 Patents Filed, 2 Patents Granted
 - Leadership Team, IBM Austin Emerging Technology & Business Innovation Board
 - Member, Out@IBM GLBT Diversity Network
 - '04 IBM Extreme Blue Technical Intern, SWG
- SKILLS**
- Embedded SW Architecture
 - C / C++ Arch & Development
 - Java6 SE
 - Object-Oriented SW Arch.
 - Leading an Agile Dev Team
 - Real-Time SW Arch.
 - Client-facing Presentation / Communication Skills
 - Hudson / Jenkins Continuous Integration
 - Gerrit / Git Src Control
 - Low-Level / Device-Level Software / HW Interfaces
 - Maven (mvn) Build Env.
 - Energy Optimization
 - Embedded Linux Dev

WORK EXPERIENCE

07/10 – Current	IBM, Systems & Technology Group, Firmware Development Organization	<i>Team Lead for a 10-person Java Development Team</i>	Austin, TX
<ul style="list-style-type: none"> • Refined requirements from stakeholders, translating into a multi-year iterative project plan, built on the Agile / Scrum development model using Rational Team Concert. • Advocated a software development methodology built on git / Gerrit for source control, Maven for build management, TestNG as a test framework, Hudson / Jenkins as Continuous Integration servers, and Sonatype Sonar as a quality metric framework. Portions of this environment are now being replicated in other parts of the organization. 			
12/08 – 07/10	IBM, Systems & Technology Group, Firmware Development Organization	<i>System Functional Owner, Power 750 / Power 755 Compute Node</i>	Austin, TX
<ul style="list-style-type: none"> • System Firmware Platform Lead for the IBM Power Systems 750 / 755, the first shipping server for the POWER7 processor. • Led firmware bringup across multiple subsystems, brokered deals with external groups, educated peers on new technologies. • Delivered firmware drivers on-time, and met manufacturing, system test, and external "extreme" early ship commitments. • Achieved platform stability several months ahead of schedule, working closely with Chief Engineer and FW Release Architect. • Primary Author / Editor, "IBM EnergyScale for POWER7 Processor-Based Systems" White Paper. 			
06/06 – 12/08	IBM, Systems & Technology Group, EnergyScale™ Technology	<i>Embedded Systems Developer, Power-Aware Firmware</i>	Austin, TX
<ul style="list-style-type: none"> • Architected and implemented real-time firmware for power management technologies on IBM Power Systems platforms, including the JS22 "POWER6" Blade, Power 520, Power 550, Power 575, and Power 595. • Productized algorithms from IBM Research for real-time performance-aware power management (Dynamic Power Optimizer) on systems with real-time Thermal / Power Management controllers (TPMD-enabled systems such as POWER 550). • Evangelized energy-efficient technology within the company, and to customers (e.g. http://www.youtube.com/watch?v=t2Bbm4tLFI). 			
06/05 – 06/06	IBM, Systems & Technology Group, Job Rotation Program	<i>Bringup Engineer, POWER6 Servers; Embedded Systems Coding</i>	Austin, TX
<ul style="list-style-type: none"> • First power-on of POWER6-based System p 570 systems, focusing on integration of firmware with POWER6 and the PowerVM. 			